

U.S. Patent Application Serial No. **10/574,848**
Amendment filed August 11, 2009
Reply to OA dated May 18, 2009

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Currently Amended): A FED control circuit for controlling an electrode voltage of a field emission display which includes a plurality of cathode electrodes and gate electrodes, both of which being arranged in a lattice shape; emitters, each of which being arranged at an intersection point of said cathode electrode and said gate electrode; fluorescent materials and anode electrodes, both of which being disposed opposing to said cathode electrode, said FED control circuit comprising:

a cathode voltage control unit for controlling said cathode electrode so that electron emission from said cathode electrode is uniform; and

a gate electrode driving unit for changing a gate electrode voltage in response to a video signal,
wherein said cathode voltage control unit charges a capacitor by a constant current and
determines a cathode voltage of each pixel by controlling charging time, and
wherein said charging time of said capacitor is controlled by pulse width.

Claims 2-3 (Canceled)

Claim 4 (Previously Presented): The FED control circuit according to claim 1, wherein

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said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 5 (Previously Presented): The FED circuit according to claim 1, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 6 (Previously Presented): The FED control circuit according to claim 2, wherein said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 7 (Previously Presented): The FED control circuit according to claim 3, wherein said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 8 (Previously Presented): The FED circuit according to claim 2, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 9 (Previously Presented): The FED circuit according to claim 3, further comprising

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a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 10 (Previously Presented): The FED circuit according to claim 4, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 11 (Previously Presented): The FED circuit according to claim 6, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 12 (Previously Presented): The FED circuit according to claim 7, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.